

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Bachelor of Technology (Computer and Communication Engineering)
Choice Based Credit System (CBCS)-2018-19

SEMESTER - I

S. No.	COURSE CODE	COURSE NAME	Teaching Scheme/Week			CREDITS	Examination Scheme					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam	Teachers Assessment* (40%)	
1	BTMACS101	Mathematics-I	3	1	0	4	60	20	20	0	0	100
2	BTPH101	Applied Physics	3	1	2	5	60	20	20	30	20	150
3	BTCC102	Introduction to Computer and Communication Engineering	3	0	2	4	60	20	20	0	50	150
4	BTEC104	Digital Logic & Circuit Design	3	1	2	5	60	20	20	30	20	150
5	BTCS104	Principles of 'C' Languages	3	0	0	3	60	20	20	0	0	100
6	BTCS108	Programming Skills with 'C'	0	0	2	1	0	0	0	30	20	50
7	BTIT307	Web Development Lab-I(XML & HTML)	0	0	4	2	0	0	0	60	40	100
			15	3	12	24	300	100	100	150	150	800

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks

*Students are advised to take online MOOC Courses of minimum 02-Credits.